

# CAI\_RS4285-SOC-01



Picture upon request  
support@embrick.de

## 1.1 Description

With this module, a corresponding IMACS host board (e.g. CPU board) can be expanded by an RS422 / RS485 interface and thus establish a data connection with other systems such as a visualization PC.

There are several jumpers on this RS22486 module with which terminating resistors are activated, the operating mode is determined, the baud rate is set and the echo is switched on or off.

JP1: Terminating resistor 120R TXB-TXA (active when jumper is inserted)

JP2: Terminating resistor 120R RXB-RXA (active when jumper is inserted)

JP3: operating mode selection (see Manual)

JP4: 9600 baud.

JP5: 19200 baud.

JP6: 38400 baud.

JP7: Echo active or inactive

## 1.2 Connectors and Indication-/Operation-Elements

Brick

Picture upon request  
[support@embrick.de](mailto:support@embrick.de)

## 1.3 Input-/Output Scheme

The following diagram shows the adaption of the control unit. To avoid overlapping, some wires are displayed interrupted and dashed.

no DocCompAdapt available

## 1.4 Technical Data

### 1.4.1 User Notes

- Blinking behavior StateLED:  
Each Morse code is 3 seconds long!  
not initialized = flashing continuously at approx. 5Hz  
no communication = short-long-short  
too little communication = short-short-short  
disturbed communication = short-long-long  
OK = continuous flashing at approx. 1Hz (0.6-1.5Hz)

## 1.5 History

On the following page you will find a list of changes that have been made to the product.

### 1.5.1 History

Date	Entry scope (HW, SWappl, SWapi, Release)	Entry type (enhancement, improvement, bugfix, release)	Version	Status (development, implemented, tested)	Responsible	Reason for the modification	Items of modification	Impact for (end-)customer	Comment	Location in model/source
xxxx-xx-xx		Release	0.99	Tested	NSt					

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