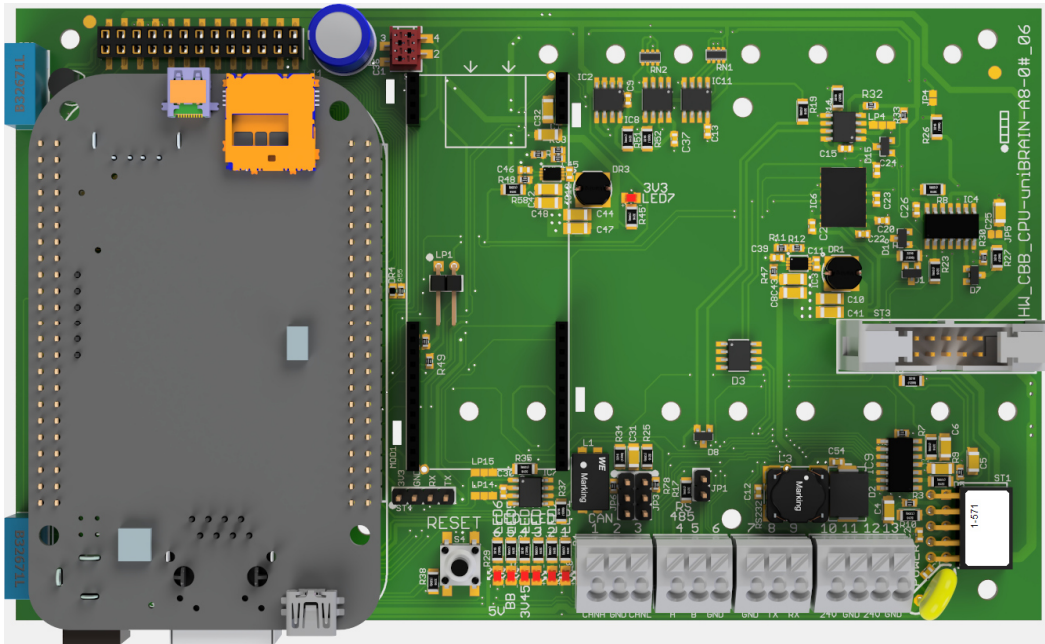


CBB_CPU-uniBRAIN-A72-01

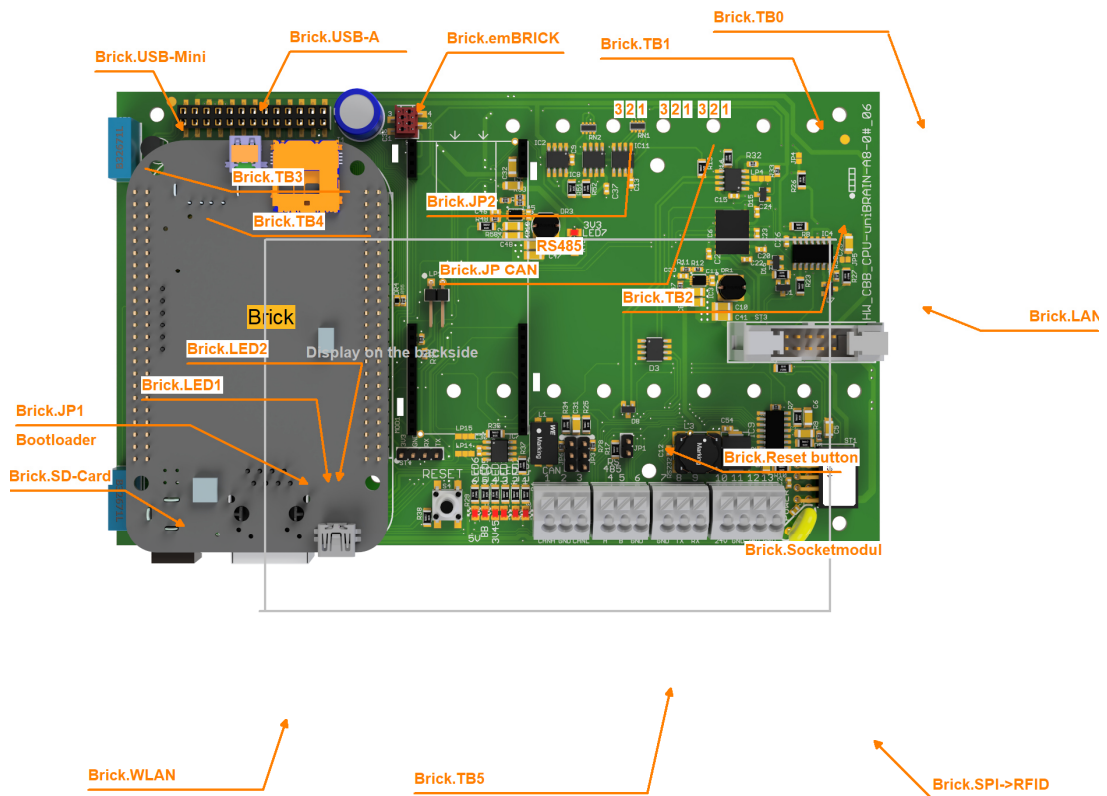


1.1 Description

The uniBRAIN-A8 is an universal, 24V driven industrial powerful CPU-base-board hosting the Beaglebone-CPU's with a Cortex-A8-Sitara, 1GHz, 4GB-Flash, 512MB-RAM
With different optional LCD front frame enclosures it becomes a compact front integration unit with a capacitive touch and 7", 10.4", 2.1" displays.

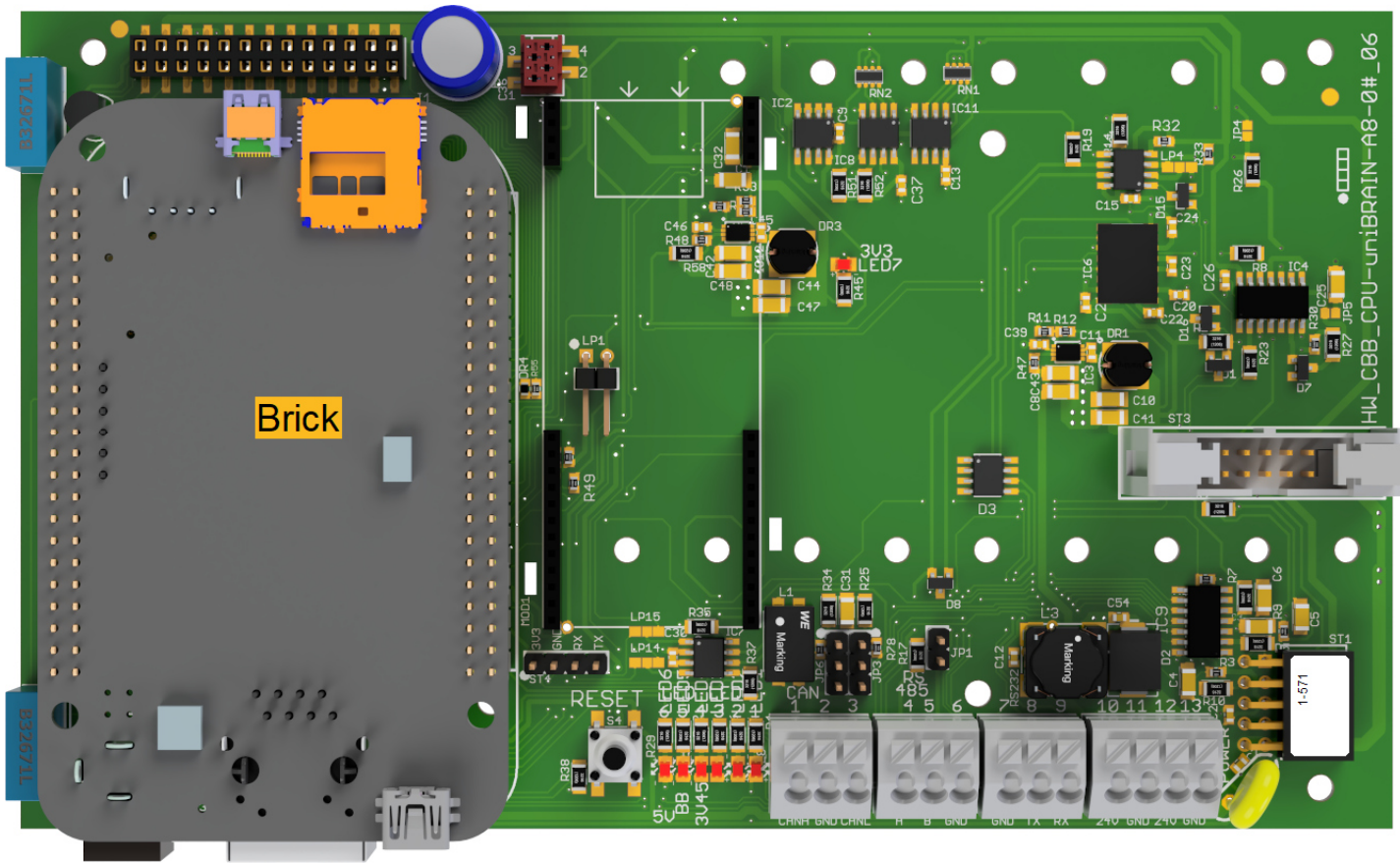
As a compact single board unit it offers: Ethernet, CAN, RS232, RS485, USB OTG, μ S-Card, separate EEPROM and a battery buffered RTC. Moreover it contains an emBRICK-Master-Port (10 pol.) to drive up to 10 emBRICK slave modules.

1.2 Connectors and Indication-/Operation-Elements



1.3 Input-/Output Scheme

The following diagram shows the adaption of the control unit. To avoid overlapping, some wires are displayed interrupted and dashed.



1.4 Technical Data

1.4.1 User Notes

- Blinking behavior StateLED:
Each Morse code is 3 seconds long!
not initialized = flashing continuously at approx. 5Hz
no communication = short-long-short
too little communication = short-short-short
disturbed communication = short-long-long
OK = continuous flashing at approx. 1Hz (0.6-1.5Hz)

1.5 History

On the following page you will find a list of changes that have been made to the product.

1.5.1 History

Date	Entry scope (HW, SWappl, SWapi, Release)	Entry type (enhancement, improvement, bugfix, release)	Version	Status (development, implemented, tested)	Responsible	Reason for the modification	Items of modification	Impact for (end-)customer	Comment	Location in model/source
xxxx-xx-xx		Release	0.99	Tested	NSt					

For questions please contact:

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